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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	ATTORNEY DOCKET NO. CONFIRMATION NO.	
10/664,475	(	09/17/2003	Steven Towle	Intel/16652	4376	
34431	7590	10/05/2004		EXAMINER		
GROSSMA		-	VU, QUANG D			
20 N. WACK SUITE 4220		/E		ART UNIT	PAPER NUMBER	
CHICAGO,	IL 6060	5	2811			

DATE MAILED: 10/05/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

			age					
	Application No.	Applicant(s)						
	10/664,475	TOWLE ET AL.						
Office Action Summary	Examiner	Art Unit						
	Quang D Vu	2811						
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period w Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	86(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this com (35 U.S.C. § 133).	munication.					
Status								
3) Since this application is in condition for allowar								
Disposition of Claims								
4)  Claim(s) 1-44 is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5)  Claim(s) is/are allowed. 6)  Claim(s) is/are rejected. 7)  Claim(s) is/are objected to. 8)  Claim(s) 1-44 are subject to restriction and/or expressions.								
Application Papers								
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) access applicant may not request that any objection to the Replacement drawing sheet(s) including the correction of the output of the contraction of the output of the contraction of	epted or b) objected to by the drawing(s) be held in abeyance. Se ion is required if the drawing(s) is ob	e 37 CFR 1.85(a). jected to. See 37 CFR	• •					
Priority under 35 U.S.C. § 119								
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applicati ity documents have been receive ı (PCT Rule 17.2(a)).	ion No ed in this National S	tage					
Attachment(s)								
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D  5) Notice of Informal F  6) Other:	ate	152)					

## **DETAILED ACTION**

## Election/Restrictions

Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 1-25, drawn to an apparatus for flip chip, classified in class 257, subclass678.
- II. Claims 26-44, drawn to a method of mounting a flip chip, classified in class 438, subclass 106.

The inventions are distinct, each from the other because of the following reasons:

Inventions II and I are related as process of making and product made. The inventions are distinct if either or both of the following can be shown: (1) that the process as claimed can be used to make other and materially different product or (2) that the product as claimed can be made by another and materially different process (MPEP § 806.05(f)). In the instant case the group I invention could be made by a material different process. For example, adding soldering flux to the flip chip to the substrate such that a surface tension of a molten solder draws the flip chip and the waveguide into engagement, instead of soldering the flip chip to the substrate such that a surface tension of a molten solder draws the flip chip and the waveguide into engagement.

Because these inventions are distinct for the reasons give above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

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Art Unit: 2811

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quang D Vu whose telephone number is 571-272-1667. The

examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Eddie Lee can be reached on 571-272-1732. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published applications

may be obtained from either Private PAIR or Public PAIR. Status information for unpublished

applications is available through Private PAIR only. For more information about the PAIR

system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

September 30, 2004

Kanshort